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| <p align="center">CERTIFICATE OF EFS FILING UNDER 37 CFR §1.8</p> <p>I hereby certify that this correspondence is being electronically transmitted to the United States Patent and Trademark Office, Commissioner for Patents, via the EFS pursuant to 37 CFR §1.8 on the below date:</p> | | |
| Date: <u>August 26, 2008</u> | Name: <u>Jasper W. Dockrey</u> | Signature: <u>/Jasper W. Dockrey/</u> |

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|--------------------------------------------|----|-----------------------------|
| In re Application of: |) | |
| |) | |
| JOLY et al. |) | |
| |) | Examiner: Zarneke, David A. |
| Serial No.: 10/561,299 |) | |
| |) | Group Art Unit: 2891 |
| Filing Date: December 15, 2005 |) | |
| |) | Confirmation No.: 3881 |
| For: INTEGRATED CIRCUIT ON HIGH- |)) | |
| PERFORMANCE CHIP |) | |
| |) | |
| Attorney Docket No.: 9905/34 (BIF023273US) | | |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

This application claims priority to the following patent applications: PCT Application Serial No. PCT/FR2004/01565, filed June 23, 2004, which claims the benefit of French patent application No. 03/07617, filed June 24, 2003. In accordance with 37 C.F.R. §1.98(d), the Examiner is directed to the references cited in all International Search Reports filed in the priority PCT application cited above, in addition to the references cited herein.

In accordance with the duty of disclosure under 37 CFR §1.56 and §§1.97-1.98, and more particularly in accordance with 37 CFR §1.97(c), Applicant hereby cites the following reference(s):

| U.S. PATENT DOCUMENTS | | |
|-----------------------|------------|---------------|
| DOCUMENT NO. | DATE | NAME |
| 6,197,695 B1 | 03/06/2001 | Joly et al. |
| 6,276,345 | 08/21/2001 | Nelson et al. |

| FOREIGN PATENT DOCUMENTS | | |
|--------------------------|------------|---------|
| DOCUMENT NO. | DATE | COUNTRY |
| EP 0807970 A1 | 13.05.1997 | EPO |

| OTHER ART – NON PATENT LITERATURE DOCUMENTS | |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
| Tillman et al., "Zero-Level Packaging for MEMS or MST Devices: The IRS Method", MEMS/Sensors, Advancing Microelectronics, Pages 37-39, November/December 2000. | |

Applicant is enclosing Form PTO-1449 (one sheet), along with a copy of each listed reference (B3-B4) for which a copy is required under 37 CFR §1.98(a)(2). As each of the listed references is in English, no further commentary is believed to be necessary, 37 C.F.R. §1.98(a)(3). Applicant respectfully requests the Examiner's consideration of the above reference(s) and entry thereof into the record of this application.

By submitting this Statement, Applicant is attempting to fully comply with the duty of candor and good faith mandated by 37 CFR §1.56. As such, this Statement is not intended to constitute an admission that any of the enclosed references, or other information referred to therein, constitutes "prior art" or is otherwise "material to patentability," as that phrase is defined in 37 CFR §1.56(a).

Applicant has calculated a processing fee in the amount of \$180.00 to be due under 37 CFR §1.17(p) in connection with the filing of this Information Disclosure Statement. Applicant has enclosed a check covering this fee, or authorized charging the fee to a deposit account or credit card, as indicated in the Transmittal accompanying this Information Disclosure Statement.

Respectfully submitted,

BRINKS
HOFER
GILSON
& LIONE

August 26, 2008
Date

/Jasper W. Dockrey/
Japser W. Dockrey (Reg. No. 33,868)